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**UTILITY PATENT APPLICATION TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 74451.P116Total Pages 3First Named Inventor or Application Identifier Maya Rani GuptaExpress Mail Label No. EL627468062US

**ADDRESS TO:** Assistant Commissioner for Patents  
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 Washington, D. C. 20231

**APPLICATION ELEMENTS**

See MPEP chapter 600 concerning utility patent application contents.

1. X Fee Transmittal Form  
(Submit an original, and a duplicate for fee processing)
2. X Specification (Total Pages 40)  
(preferred arrangement set forth below)
  - Descriptive Title of the Invention
  - Cross References to Related Applications
  - Statement Regarding Fed sponsored R & D
  - Reference to Microfiche Appendix
  - Background of the Invention
  - Brief Summary of the Invention
  - Brief Description of the Drawings (if filed)
  - Detailed Description
  - Claims
  - Abstract of the Disclosure
3. X Drawings(s) (35 USC 113) (Total Sheets 6)
4. X Oath or Declaration (Total Pages 5)
  - a.      Newly Executed (Original or Copy)
  - b.      Copy from a Prior Application (37 CFR 1.63(d))  
(for Continuation/Divisional with Box 17 completed) (**Note Box 5 below**)
  - i.      DELETIONS OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
5.      Incorporation By Reference (useable if Box 4b is checked)  
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6.      Microfiche Computer Program (Appendix)

06/20/00  
 JC852 U.S. PTO

06/20/00  
 JC490 U.S. PTO  
 09/597861

7. ☐ Nucleotide and/or Amino Acid Sequence Submission  
(if applicable, all necessary)  
a. ☐ Computer Readable Copy  
b. ☐ Paper Copy (identical to computer copy)  
c. ☐ Statement verifying identity of above copies

**ACCOMPANYING APPLICATION PARTS**

8. ☐ Assignment Papers (cover sheet & documents(s))  
9. ☐ a. 37 CFR 3.73(b) Statement (where there is an assignee)  
☐ b. Power of Attorney  
10. ☐ English Translation Document (if applicable)  
11. ☐ a. Information Disclosure Statement (IDS)/PTO-1449  
☐ b. Copies of IDS Citations  
12. ☐ Preliminary Amendment  
13. ☒ Return Receipt Postcard (MPEP 503) (Should be specifically itemized)  
14. ☐ a. Small Entity Statement(s)  
☐ b. Statement filed in prior application, Status still proper and desired  
15. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)  
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- 2 -


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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: June 20, 2000

  
\_\_\_\_\_  
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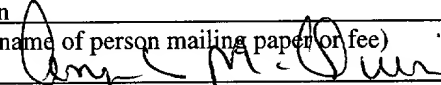
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Serial/Patent No.: \*\*\* Filing/Issue Date: \*\*\*  
Client: Ricoh Corporation  
Title: A BLOCK QUANTIZATION METHOD FOR COLOR HALFTONING

BSTZ File No.: 74451.P116

Atty/Secty Initials: MJM/amq

Date Mailed: 6/20/00

Docket Due Date: \*\*\*

The following has been received in the U.S. Patent & Trademark Office on the date stamped hereon:

- ☐ Amendment/Response (\_\_\_\_ pgs.)  
☐ Appeal Brief (\_\_\_\_ pgs.) (in triplicate)  
☒ Application - Utility (40 pgs., with cover and abstract)  
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UNITED STATES PATENT APPLICATION

for

A BLOCK QUANTIZATION METHOD FOR COLOR HALFTONING

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## **A BLOCK QUANTIZATION METHOD FOR COLOR HALFTONING**

### **FIELD OF THE INVENTION**

The present invention relates to the field of color halftoning; more particularly, the present invention relates to applying a block quantization  
5 technique to color halftoning.

### **BACKGROUND OF THE INVENTION**

Color halftoning attempts to represent an image in multi-bit (e.g., 24 bits) color as an image with only a few colors and possibly increased spatial  
10 resolution. This is a problem faced by many color printers which commonly have only 5 or 8 colors available, such as, for example, cyan, magenta, yellow, black, white, and the combinations of cyan + magenta, cyan + yellow, magenta + yellow.

Judging color halftoning algorithms can be difficult due to the  
15 subjective nature of vision. Specific goals in halftoning algorithms include color matching, simple computation, low graininess, artifact minimization, and reduction of post-processing effects. The relative importance of each of these goals depends upon the human viewer, the expected viewing distance, and the target image.

20 Color matching itself has at least two objectives: closeness of individual input and output colors and consistent errors. Computation

issues include running speed, parallelizability, memory size, and complexity. Graininess can be described as the ability to perceive distinct output colors (instead of the colors blending perfectly). Perceived graininess is a function, in part, of the dpi.

5           Detail information may be lost due to either poor spatial resolution or poor color resolution (or a combination of both). Artifacts are a general term for anything not desirable that is a result of the algorithm (as opposed to noise), including speckles, streaks, ghosts, gridding, texture, and very importantly, false contouring (also known as banding).

10           Post-processing effects are a concern because a halftoned image is not necessarily 'finished' when it is halftoned. Various image processing operations may still be performed on it, from downsampling to compression to edge enhancement. A halftoning algorithm may affect the success and ease of these operations.

15           Different techniques have been devised for halftoning, including dithering, error diffusion and iterative methods. Most color halftoning techniques simply extend a black-and-white halftoning technique to each of the three color planes individually. While computationally efficient, such component-wise extensions implicitly assume a mathematical linearity of



ink colors. However, the subtractive color mixing in printing is nonlinear, and thus such plane-by-plane halftoning methods are suboptimal.

Dithering consists of adding a pseudorandom 2D periodic signal to each color component of the image, then quantizing each pixel to the closest  
 5 output color. The dither signal is designed to generate patterns of alternating colors that blend to the eye. Different styles of dithering include clustered dot dithering, distributed dot dithering, and ordered dot dithering. Dithering with white noise does not achieve results as visually pleasing as more ordered dithering. Dithering is known to suffer from noticeable  
 10 periodic patterning. Many efforts have tried to improve dithering. The application of dithering to color is in the art. For more information, see A. Karun, et al., "New Methods for Dithering of Color Images," Proc. of the IEEE Workshop on Nonlinear Signal/Image Processing, 1995.

While dithering is a point process (i.e., the output at each pixel  
 15 depends only on the input pixel and possibly external factors like the dither signal), error diffusion diffuses the quantization error at each pixel to its neighboring pixels based on a directional spatial filter that sends a portion of the error in each forward direction. As a result, the error diffusion halftoning process adds high frequency noise. Since the goal is for the eye to

blend the colors and not notice the actual mosaic of colors, this highpass filter effect of error diffusion exploits the eyes' insensitivity to high frequency noise. Thus, error diffusion yields a blue spatial spectrum. The idea that this blue noise is more visually pleasing is well-established.

- 5           Error diffusion works well. However, error diffusion has some drawbacks. It is a greedy algorithm in that it locally optimizes, that is, it does the best job it can at each pixel in space. Since the error is diffused forward, it is directional and cannot provide true joint optimization over a local region. An error may get diffused far beyond the point at which the
- 10   eye is averaging color. Error diffusion artifacts include speckles, streaks, transient and edge effects, and grid effects at certain intensities. The feedback loop required for error diffusion can be a hindrance to parallel implementation of the algorithm.

Improvements have been made in error diffusion over many years.

- 15   Efforts have been made in such directions as minimizing structural artifacts, making the feedback filter adaptive, and using neural networks.

Also, there are successful black-and-white halftoning techniques that use perceptual and printer models, but these require complex extensions for color halftoning.

There are additional black and white algorithms that consider the idea of mean-matching. For example, one multiscale halftoning algorithm begins at the resolution of the whole image and sets the output image average gray level equal to the input image average gray level. Then the

5 next higher level of resolution proportionately splits up the average gray level amongst four quadrants based on their average gray levels. This process continues through the levels of resolution until each pixel is quantized black or white. For example, see Breaux, et al., "Wavelet Methods for Compression, Rendering, and Descreening in Digital Halftoning," SPIE

10 Vol. 3078, 1997; P.W. Wong, "Halftoning by Multiscale Dot Distribution," Proc. IEEE Int. Conference on Image Processing, Vol. III, 1995; P.W. Wong, "Mean matching Halftoning on a Rotated Quad-tree Structure," Human Vision and Electronic Imaging," Proc. SPIE 2657, 1996.

In another prior art solution, the average gray levels of the input and

15 output images are generated at various resolutions. In still another embodiment, wavelet averages are calculated and then dithering is used to approximate these gray-level averages. More specifically, the image is first compressed with wavelets and then the wavelet coefficients are used at each level as the average gray level to determine how white dots should be

[illegible]

### SUMMARY OF THE INVENTION

A method and apparatus for block quantization for color halftoning is described. In one embodiment, the method comprises dividing an input image and an output image into blocks, wherein each block in the output  
5 image corresponds to one block in the input image. The method also includes dividing each block into subblocks according to a criteria, calculating a color average of each input block, and calculating a set of output colors for each block in the output image to match the color average of its corresponding block in the input image.

**BRIEF DESCRIPTION OF THE DRAWINGS**

The present invention will be understood more fully from the detailed description given below and from the accompanying drawings of various embodiments of the invention, which, however, should not be taken  
5 to limit the invention to the specific embodiments, but are for explanation and understanding only.

**Figure 1** is a block diagram of one embodiment of block quantization system for halftoning.

10

**Figure 2** is a flow diagram of one embodiment of the process for implementing block quantization with halftoning.

**Figure 3** illustrates potential output color sets.

15

**Figure 4** is a block diagram of one embodiment of a system having block quantization processing.

**Figure 5A and 5B** illustrates laying out a chosen color set.

20

**Figure 6** is an example of weighting.

## DETAILED DESCRIPTION

A block quantization method and apparatus are described. In the following description, numerous details are set forth. It will be apparent, however, to one skilled in the art, that the present invention may be  
5 practiced without these specific details. In other instances, well-known structures and devices are shown in block diagram form, rather than in detail, in order to avoid obscuring the present invention.

Some portions of the detailed descriptions that follow are presented in terms of algorithms and symbolic representations of operations on data  
10 bits within a computer memory. These algorithmic descriptions and representations are the means used by those skilled in the data processing arts to most effectively convey the substance of their work to others skilled in the art. An algorithm is here, and generally, conceived to be a self-consistent sequence of steps leading to a desired result. The steps are those  
15 requiring physical manipulations of physical quantities. Usually, though not necessarily, these quantities take the form of electrical or magnetic signals capable of being stored, transferred, combined, compared, and otherwise manipulated. It has proven convenient at times, principally for

reasons of common usage, to refer to these signals as bits, values, elements, symbols, characters, terms, numbers, or the like.

It should be borne in mind, however, that all of these and similar terms are to be associated with the appropriate physical quantities and are merely convenient labels applied to these quantities. Unless specifically  
5 stated otherwise as apparent from the following discussion, it is appreciated that throughout the description, discussions utilizing terms such as "processing" or "computing" or "calculating" or "determining" or "displaying" or the like, refer to the action and processes of a computer  
10 system, or similar electronic computing device, that manipulates and transforms data represented as physical (electronic) quantities within the computer system's registers and memories into other data similarly represented as physical quantities within the computer system memories or registers or other such information storage, transmission or display devices.

15 The present invention also relates to apparatus for performing the operations herein. This apparatus may be specially constructed for the required purposes, or it may comprise a general purpose computer selectively activated or reconfigured by a computer program stored in the computer. Such a computer program may be stored in a computer readable



storage medium, such as, but is not limited to, any type of disk including floppy disks, optical disks, CD-ROMs, and magnetic-optical disks, read-only memories (ROMs), random access memories (RAMs), EPROMs, EEPROMs, magnetic or optical cards, or any type of media suitable for storing electronic  
5 instructions, and each coupled to a computer system bus.

The algorithms and displays presented herein are not inherently related to any particular computer or other apparatus. Various general purpose systems may be used with programs in accordance with the teachings herein, or it may prove convenient to construct more specialized  
10 apparatus to perform the required method steps. The required structure for a variety of these systems will appear from the description below. In addition, the present invention is not described with reference to any particular programming language. It will be appreciated that a variety of programming languages may be used to implement the teachings of the  
15 invention as described herein.

A machine-readable medium includes any mechanism for storing or transmitting information in a form readable by a machine (e.g., a computer). For example, a machine-readable medium includes read only memory ("ROM"); random access memory ("RAM"); magnetic disk storage media;

optical storage media; flash memory devices; electrical, optical, acoustical or other form of propagated signals (e.g., carrier waves, infrared signals, digital signals, etc.); etc.

## 5 Overview of Block Color Quantization

The present invention performs color halftoning using block quantization with adaptively sized input blocks. Figure 1 is a block diagram of one embodiment of a block quantization system for color halftoning. The block quantization system performs a color match between the input domain of the system and the output domain of the system to achieve color halftoning where the number of colors in the input domain is greater than the number of available colors in the output domain. The system performs the color matching by dividing images in the input and output domains into multiple blocks and matching the color average of each block in the image in the input domain with a color average for a block in the image in the output domain. The blocks may be any shape, including complex shapes that are combinations of two or more shapes. For example, the blocks may be a square ( $d \times d$ ), but they may be rectangular ( $d \times b$ ) where  $d$  and  $b$  are not equal.

In one embodiment, the input domain comprises a computer system having a monitor and the output domain comprises a printer. For each small block of an input image, the block quantization technique described herein computes a set of the printing colors for a corresponding output block that best renders (under a principled distance measure in one embodiment) the input block's color average. In this manner, the halftoning technique described herein ensures that for each local input region the average color matches the average color of the corresponding local output region, where the regions are small enough to ensure that the eye averages over them. The block quantization technique exploits the higher spatial resolution of the printing process to achieve better color resolution and yields constrained local color optimality. Speckle, texture, and other artifacts common with error-diffusion methods are substantially avoided.

The technique treats each input color as a 3-dimensional (3D) vector in color space and, in one embodiment, generates halftones using the actual output colors obtained in a subtractive printing process.

Referring to Figure 1, an input image 101 is received by a block creation and color averaging unit 102 that divides input image 101 into

blocks and generates an average color value for each of the blocks. In one embodiment, the blocks are fixed-size.

Block creation and color averaging unit 102 may include adaptive block sizing unit 103 that further divides an individual block into sub-blocks if the individual block contains an edge. In one embodiment, the number of sub-blocks is four; however, any number of sub-blocks may be used.

Adaptive block sizing unit 103 may iterate on the blocks, further dividing each individual block, which may be a fixed size, into sub-blocks until each block does not contain an edge or meets some other specific criteria. In alternative embodiments, one, more, or all blocks may contain an edge and still not be divided into subblocks. In one embodiment, a block containing an edge may not be divided into subblocks in cases, for example, where dividing the block further causes lower color resolution. Note that such an embodiment requires feedback which has not been shown in Figure 1 to avoid obscuring the present invention.

In one embodiment, to determine if each block contains an edge, the average vector color value is calculated and compared to the averages of multiple (e.g., four) sub-blocks in the block. If the difference between the block and the averages of sub-block passes a threshold, then the block is

deemed to have an edge and is further divided into the constituent sub-blocks. This may be repeated until blocks are a predetermined size. For example, when initially using 4x4 block pixels, the adaptive block sizing may continue until the blocks are 1x1 blocks of pixels.

- 5           Given an input block's color average as a target, a color matching and layout unit 104 computes a set of output colors that will average as close as possible to the target value. In one embodiment, color matching and layout unit 104 computes the output color set by examining the possible values achievable in the color space of the output image and identifying the closest
- 10   achievable value to the target.

          Given the output color set, color matching and layout unit 104 positioning the colors to achieve the desired color and to reduce, and maybe even minimize, artifacts and graininess.

- Figure 2 is a flow diagram of one embodiment of the processing
- 15   operations of the block quantization technique described herein. The process is performed by processing logic that may comprise hardware, software or a combination of both.

          Referring to Figure 2, the process begins with processing logic selecting a block size that the eye will average over in the input domain

(processing block 201). In one embodiment, the block size is square ( $d \times d$ ). In alternative embodiments, the block size is not square (e.g., a rectangle, diamond, etc.).

Next, processing logic divides the input and output images into  
 5 corresponding blocks (processing block 202). For each input block there is a corresponding output block with  $n$  pixels (where  $n$  depends on the output spatial resolution and the size of the input block).

Then, processing logic determines if there is an edge within each  
 block (processing block 203). There are many ways to determine if there is  
 10 an edge within a block. As discussed above, in one embodiment, an edge is present if the difference between the color average of the block and its sub-blocks exceeds a threshold. If there is an edge, processing logic divides that block into sub-blocks with no edge (processing block 204). This is referred to herein as quadtreeing.

15 In one embodiment, four-by-four blocks of pixels are first considered, and then the input average of the entire  $4 \times 4$  block is compared to the averages of the  $2 \times 2$  sub-blocks in order to determine whether or not there is a spatial edge in the input image as part of a thresholding process. If it is deemed that there is an edge, a color match is found independently for each

2x2 block. If the thresholding process determines there is no discernible edge, then the 4x4 block is color quantized as a whole. In one embodiment, this process is repeated until a 1x1 block size is considered.

- In one embodiment, the thresholding is based on the Euclidean
- 5 distance between the 4x4 average and each of the 2x2 averages. If the Euclidean distance is over the threshold for any of the 4 sub-blocks than the 4x4 block is broken up. In general, thresholding is based on a function of the color values in some colorspace (e.g., RGB, or some other color space). Thresholding may compare subblock values to each other or to the whole
- 10 block's value.

- Once the blocks no longer need to be broken into subblocks, and assuming such an operation has not occurred earlier, processing logic calculates the color average  $A_i$  of each input block (processing block 205). In one embodiment, an arithmetic average over the block (equivalent to a 2D
- 15 Haar wavelet) is used to generate a value for each block indicative of its color. A different wavelet or eye filter model may be used to define the color average of blocks in the input domain, or a different sense of average may be used, such as, for example, a geometric average or L2 average.

Furthermore, the average may be taken over the Cartesian coordinates or over another indexing of the space, such as polar coordinates.

Next, processing logic identifies a set of colors in the output domain that most closely match mathematically color averages of blocks of the input image (processing block 206). In one embodiment, processing logic computes integer coefficients  $\beta_i$  over the basis set of output colors  $B$  represented by the following equation of a distance measure:

$$\arg \min \text{dist}[\text{Avg}(\beta_i \cdot B), A_i] \quad (1)$$

This operation is constrained so that each of the  $n$  output pixels is assigned a color. Thus,

$$\sum_{i=1}^b \beta_i = n \quad (2)$$

where " $n$ " is the number of pixels in the output block and " $b$ " is the number of output colors. The same color averaging technique (equation 1 above) that is used to determine the averages of the blocks in the input image may be used as well. This process reduces, and may even minimize, a distance between the colors available in the input domain and the colors available in the output domain. Note that various distance measures may be used such as, for example, Euclidean, Manhattan, the  $L_1$ ,  $L_2$ , and  $L_\infty$  norms.



Finally, given the set of output colors  $\beta_i \cdot B$  for a given output block, processing logic "mosaics" the chosen set of colors within the block to create an image in the output domain (processing block 207). In one embodiment, the processing logic attempts to best match the color of the corresponding

5 block in the input image and reduce, and maybe even minimize, spatial artifacts. Given the set of output colors  $\beta_i \cdot B$  for a given output block, the arrangement of the colors within the block has a significant effect on spatial artifacts and perceived color. In one embodiment, random sampling, regular gridding, and arranging the colors using a blue noise dither matrix

10 are used, independently or adaptively switching, to achieve visually pleasing effects. Note that an indication may be generated by processing logic, which it sends to an output device (e.g., print engine) to dictate how the output device is to position the chosen set of colors on the piece of paper.

The process is applicable in various color spaces, including, for

15 example, RGB and CIELAB.

The block quantization technique achieves constrained local color optimality and does not suffer error-diffusion type artifacts. Color matching, spatial resolution, and color resolution rival that of error diffusion. Solving the minimization problem is the computationally

intensive part of the algorithm and may be sped up by the use of integer programming, dynamic programming, or a look-up table. For instance, a simple but memory intensive look-up table would take a block color value as input and return the number of pixels to use of each of the outputs colors for that block. For example, for a monitor to 4 color printer quantization, an input block might have an average RGB value of (122, 33, 250). This could be input to a look-up-table for blocks of size 16 output pixels. The look-up table might return: 13 cyan, 3 magenta, 0 yellow, 0 black and 0 white. Also, the algorithm may be sped up by finding approximate solutions, for instance, by framing the problem as a matrix inversion and solving it via a maximum entropy method or linear programming method or an approximate look-up-table could be used. Or it could be solved approximately by dynamic programming.

## 15 A Spatial Filter and Alternatives

An aim of certain embodiments of the block quantization techniques described herein is to match the average color of a local input region (e.g., block) to the average color of the corresponding local output region. The average of a local region can be found by applying a spatial filter over the

local region, weighting the values. An example of weighting is shown in Figure 6. For example, a rect filter may be used. If so, the pixel in the region is counted evenly with all the other pixels in the region.

A non-Rect filter may be used to compute the average in the input  
 5 domain and/or to compute the output color set.

A wavelet filter may be used to determine the average. In one embodiment, the high-pass wavelet components may be used to determine whether or not there was an edge. In this manner, the high-pass wavelet filters are used for the quadtree thresholding.

10 The calculated average depends on the color space used. Different color spaces may be used. Color spaces include, but are not limited to, RGB, CIELAB, CIELUV, as well as Ohta, Opponent, log Opponent, HSV, HUV, and YCbCr.

The shapes of input and output regions may be viewed with a  
 15 preference towards circular regions. Those shapes that strayed from a general circular form generally provide less favorable results.

The size of the regions is selected to avoid considering regions too large, resulting in a loss of the validity of the eye averaging assumption. Regions too small restrict color resolution. Regions too big cause loss of

ability to resolve spatial detail. Thus, within the eye averaging size, there is a trade-off between color resolution and spatial resolution, neglecting either may cause loss in detail information.

## 5 Choosing the Set of Output Colors

A method to find a mathematically optimal set of  $n$  output colors from  $B$  possible colors is described below. First, consider a simpler problem of choosing  $n=3$  output colors from a 2D color space delimited by  $B=3$  possible colors: J, K, and L. Referring to Figure 3, the x's mark potential choices for the set of  $n$  chosen output colors: JJJ, JJK, JKK, etc. Two vectors JK and JL may be defined which are  $1/n$  the length of the distance between the "anchors" J, K, and L. The vectors JK and JL form a vector basis that spans the 2D color space.

Gram-Schmidt orthogonalization of the vector basis may be used to generate an orthogonal vector basis. A given input target color point can be imagined as a vector from the origin J to the target. This J target vector can be decomposed onto the orthogonal basis and reconstituted as coefficients of the original JK and JL vectors. These two coefficients specify how many output colors should be K and how many output colors should be L (then

the remaining colors in the set of output colors should be J). The Gram-Schmidt process results in an exact breakdown of  $n$  colors into J, K, and L to achieve the target. However, only integer solutions may be used. Therefore, in one embodiment, the closest integer solution is chosen as the

5 output color set.

With a 3D color space, the same vector technique can be used to solve for  $n$  with  $B$  equal to up to four possible colors. However, with more than four colors in 3D or more than three colors in 2D, the vector basis is redundant (e.g., the problem of decomposing the target onto the original

10 vectors exactly is undetermined, so there are many solutions although not necessarily any exact integer solutions).

For more than four colors, one solution includes considering each possible integer number of the fifth color 0 through  $n$ , decompose the (target average - fifth color contribution) onto the remaining four colors. Thus, for

15 five colors, the four color solution is found. A close integer solution is typically found unless the target color is out of the convex hull formed by the output inks.

A linear programming formulation may be used to find the appropriate set of output colors to match an input target. Linear programming

searches for a solution that reduces, or may be even minimizes, the distance between the target color and the output color average, but the distance reduced (minimized) cannot be the Euclidean distance (equal L2 norm squared). Instead, linear programming reduces the L1 or sup norm.

- 5 Quadratic programming may be used to reduce, or may be even minimize, the Euclidean distance.

A table look up may be used to speed up the process of choosing an output set. This is particularly advantageous in selections where the output colors are known beforehand, such as with, for example, printers. In one  
 10 embodiment, the LUT has a memory cell for every possible input color. In an alternative embodiment, the LUT is programmed with the different output color sets and/or an input color locates the closest output color. This requires less memory, more processing, and may give inexact answers.

- 15 In one embodiment, the colorspace is divided into quantifiable cells beforehand. All input averages that fall in a given cell are quantified to that cell's centerpoint.

As discussed above, in one embodiment, given an input vector color average, an output set of colors is chosen to reduce or minimize the Euclidean distance between the input average and the chosen set of output

colors. In alternative embodiments, the Manhattan distance or Component-Wise Max may be reduced, or may be even minimized.

As discussed above, an average is taken over the set of input colors and the set of chosen output colors, then the Euclidean distance is reduced (and may be minimized). Instead of computing an average for each block, a distance measure (e.g., the Earth Mover's Distance (EMD)) can be used to determine the distance between the input block and the proposed output colors. In one embodiment, EMD is used to select between a few possibilities that were quickly generated by some quicker output set generator. For more information on the EMD, see Rubnax, Tomasi, Goibas, ICCV 1998.

### **Tessellating the Output Colors**

Given a chosen set of output colors, the problem remains to decide how to position them down in the output space. For example, positioning 3 red pixels and 3 green pixels as

RRR  
GGG

or

RGR

## GRG

may influence the perceived color, the graininess, and the amount of false  
 contouring. A number of different tessellation schemes may be used,  
 including, for example, regular gridding, and random sampling (with and  
 5 without replacement), and using the values of a blue dither matrix to order  
 the chosen output pixels.

Regular gridding can be performed in two ways. One way is to lay  
 all the pixels of a given output color out in the block at once, then do the  
 next output color, and so on. For example, 8R and 8G would become:

10                   RRRR  
                     RRRR  
                     GGGG  
                     GGGG

This method avoids isolated colors. In an alternative embodiment, a regular  
 15 grid may be used that alternates as much as possible. This yields well-  
 blended images. In this case the 8R and 8G becomes

20                   RGRG  
                     GRGR  
                     RGRG  
                     GRGR

Another way to tessellate a block is random sampling. Given that 8R  
 and 8G are to be placed in a block, for each pixel of the output block that this



distribution may be randomly sampled, with or without replacement. This tends to lead to grainy-looking images however.

A visually pleasing way to tessellate is to lay the chosen output colors down in a blue spatial frequency pattern. One way to achieve this is to choose a dither matrix designed for this purpose. The output colors are ordered in some fashion, such as, for example, by luminance. For example, the output colors could be WYCMK, where K is black. Then lay the chosen output colors down in the dither order. Figures 5A and 5B illustrate an example. If a 4x4 dither matrix looks like Figure 5A, then the chosen set of output colors looks like the matrix in Figure 5B.

Dither matrices larger than a given block can be used, but then a block just uses the ordering of the values it covers.

The average may be represented better at all resolutions using either a deterministic approach or a stochastic approach that tries to more closely match the averages of a block's subblocks (when the block wasn't split as discussed above with respect to thresholding) . For 4x4 blocks, after the 16 output colors are chosen, they can be divided up into four groups of four based on the average of each of the 2x2 subblocks.

## Exemplary Applications

In one embodiment, the block color quantization is performed by an embedded processor in a system, such as, for example, a copier or a printer. The algorithm might be on an application specific integrated circuit (ASIC).

- 5 Figure 4 is a block diagram of an exemplary system that may include processing for the block quantization technique described above.

- Referring to Figure 4, system 400 comprises an embedded processor 412 coupled with the embedded processor 412 for processing information. System 400 further comprises a random access memory (RAM), or other
- 10 dynamic storage device 404 coupled to embedded processor 412 for storing information and instructions to be executed by embedded processor 412.

- Computer system 400 may further be coupled to a display device 421, such as a cathode ray tube (CRT) or liquid crystal display (LCD), coupled to embedded processor 412 for displaying information to a system user. An
- 15 alphanumeric input device, including alphanumeric and other keys, may also be included in the system for communicating information and command selections to embedded processor 412. An additional user input device is cursor control, such as a mouse, trackball, trackpad, stylus, or cursor direction keys, may be included in the system for communicating

direction information and command selections to processor 412, and for controlling cursor movement on display 421.

A print engine 424 is coupled to embedded processor 412. In one embodiment, the print engine 424 is part of a copier. Note that any or all of the components of system 400 and associated hardware may be used in the present invention. However, it can be appreciated that other configurations of the system may include some or all of the devices.

Whereas many alterations and modifications of the present invention will no doubt become apparent to a person of ordinary skill in the art after having read the foregoing description, it is to be understood that any particular embodiment shown and described by way of illustration is in no way intended to be considered limiting. Therefore, references to details of various embodiments are not intended to limit the scope of the claims which in themselves recite only those features regarded as essential to the invention.

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## CLAIMS

We claim:

- 1           1.     A method comprising:  
2           dividing an input image and an output image into blocks, wherein  
3     each block in the output image corresponds to one block in the input image;  
4           calculating a color average of each input block;  
5           calculating a set of output colors for each block in the output image to  
6     match the color average of its corresponding block in the input image; and  
7           generating an indication to control positioning of the set of output  
8     colors in said each block of the output image.
- 1           2.     The method defined in Claim 1 further comprising dividing  
2     each input and output block into subblocks in response to a certain criteria  
3     being met.
- 1           3.     The method defined in Claim 2 wherein the criteria for  
2     dividing a block into subblocks is whether the block contains an edge.
- 1           4.     The method defined in Claim 1 wherein characteristics of the  
2     block are such that a human eye averages colors associated with the block.

1           5.     The method defined in Claim 4 wherein the characteristics  
2 include one or more of size, shape and expected viewing distance.

1           6.     The method defined in Claim 1 wherein calculating the set of  
2 output colors is performed by examining possible values achievable in a  
3 color space and locating an achievable value closest to the color average of  
4 the corresponding input block.

1           7.     The method defined in Claim 1 wherein calculating the set of  
2 output colors comprises using a look-up table on the average color to  
3 determine output colors in the set.

1           8.     The method defined in Claim 7 wherein the average color  
2 distance output color is the set either exactly or approximately.

1           9.     The method defined in Claim 1 wherein calculating the set of  
2 output colors comprises using integer programming.

1           10.    The method defined in Claim 1 wherein calculating the set of  
2 output colors comprises using dynamic programming.

1           11.    The method defined in Claim 1 wherein calculating the set of  
2 output colors comprises using an approximate linear programming solution.

1           12.    The method defined in Claim 1 wherein calculating the set of  
2   output colors comprises using an approximate maximum entropy solution.

1           13.    The method defined in Claim 1 wherein dividing a block into  
2   subblocks comprises:  
3           calculating a function of the color value for each block and for each of  
4   a plurality of subblocks in said each block;  
5           determining if the distance between the function of the color value of  
6   the block and subblocks is greater than a threshold; and  
7           dividing the block into subblocks if the difference is greater than the  
8   threshold.

1           14.    The method defined in Claim 13 further comprising  
2   determining an edge exists by comparing a function of color values for each  
3   subblock to the function's value for the other subblocks of the block.

1           15.    The method defined in Claim 13 wherein the plurality of  
2   subblocks comprises four subblocks.

1           16.    The method defined in Claim 1 further comprising positioning  
2   colors within each block to match a target color and reduce spatial artifacts  
3   given the set of output colors.

1           17.    The method defined in Claim 16 wherein tessellating colors  
2    within each block comprises performing regular gridding.  
3

1           18.    The method defined in Claim 16 wherein tessellating colors  
2    within each block comprises using a dither matrix ordering of the chosen  
3    output colors.

          19.    An article of manufacture having a recordable medium storing  
executable instructions which, when executed by a system causes the system  
to perform the steps of Claim 1.

1           20.    An apparatus comprising:  
2           a divider to divide an input image and an output image into blocks,  
3    wherein each block in the output image corresponds to one block in the  
4    input image;  
5           first logic to calculate a color average for each input block;  
6           second logic to calculate a set of output colors for each block in the  
7    output image to match the color average of its corresponding block in the  
8    input image; and  
9           indication generation logic to generate an indication to control  
10   positioning of the set of output colors in said each block of the output image.

1           21.    The apparatus defined in Claim 20 wherein the divider divides  
2   each input and output block into subblocks in response to a certain criteria  
3   being met.

1           22.    The apparatus defined in Claim 20 wherein the criteria for  
2   dividing a block into subblocks is whether the block contains an edge.

1           23.    The apparatus defined in Claim 19 wherein characteristics of  
2   the block are such that a human eye averages colors associated with the  
3   block.

1           24.    The apparatus defined in Claim 23 wherein the characteristics  
2   include one or more of size, shape and expected viewing distance.

1           25.    The apparatus defined in Claim 19 wherein the logic to  
2   calculate the set of output colors examines possible values achievable in a  
3   color space and locates an achievable value closest to the color average of the  
4   corresponding input block.

1           26.    The apparatus defined in Claim 19 wherein the logic to  
2   calculate the set of output colors uses a look-up table on the average color to  
3   determine output colors in the set.



1           27.    The apparatus defined in Claim 26 wherein the average color  
2   distance output color is the set either exactly or approximately.

1           28.    The apparatus defined in Claim 19 wherein the logic to  
2   calculate the set of output colors uses integer programming.

1           29.    The apparatus defined in Claim 19 wherein the logic to  
2   calculate the set of output colors uses dynamic programming.

1           30.    The apparatus defined in Claim 19 wherein the logic to  
2   calculate the set of output colors uses an approximate linear programming  
3   solution.

1           31.    The apparatus defined in Claim 19 wherein the logic to  
2   calculate the set of output colors uses an approximate maximum entropy  
3   solution.

1           32.    The apparatus defined in Claim 19 further comprises:  
2       calculation logic to generate a function of the color value for each  
3   block and for each of a plurality of subblocks in said each block;  
4       determination logic to determine if the distance between the function  
5   of the color value of the block and subblocks is greater than a threshold; and

6 a divider to divide the block into subblocks if the difference is greater  
7 than the threshold.

1 33. The apparatus defined in Claim 32 further comprising  
2 determination logic to determine an edge exists by comparing a function of  
3 color values for each subblock to the function's value for the other subblocks  
4 of the block.

1 34. The apparatus defined in Claim 32 wherein the plurality of  
2 subblocks comprises four subblocks.

1 35. The apparatus defined in Claim 19 further comprising control  
2 logic to position colors within each block to match a target color and reduce  
3 spatial artifacts given the set of output colors.

1 36. The apparatus defined in Claim 35 wherein the control logic  
2 performs regular gridding within each block.

1 37. The apparatus defined in Claim 36 wherein the control logic  
2 performs dither matrix ordering of the chosen output colors within each  
3 block.

1 38. An apparatus comprising:

2 means for dividing an input image and an output image into blocks,  
3 wherein each block in the output image corresponds to one block in the  
4 input image;

5 means for calculating a color average of each input block;

6 means for calculating a set of output colors for each block in the  
7 output image to match the color average of its corresponding block in the  
8 input image; and

9 means for generating an indication to control positioning of the set of  
10 output colors in said each block of the output image.

11

1 39. A method comprising:

2 dividing the input image and the output image into blocks comprises  
3 adaptively sizing blocks with edges to create a plurality of blocks without  
4 edges; and

5 computing a set of output colors that best renders a color average of  
6 the input image for the corresponding block.

1 40. The method defined in Claim 39 wherein the input image's  
2 color gamut is pre-warped to adjust for the output color gamut.

1 41. An apparatus comprising:

2 means for dividing the input image and the output image into blocks  
3 comprises means for adaptively sizing blocks with edges to create a plurality  
4 of blocks without edges; and  
5 means for computing a set of output colors that best renders a color  
6 average of the input image for the corresponding block.

1 42. The apparatus defined in Claim 39 wherein the input image's  
2 color gamut is pre-warped to adjust for the output color gamut.

1 43. An apparatus comprising:  
2 a divider to divide the input image and the output image into blocks  
3 by means for adaptively sizing blocks with edges to create a plurality of  
4 blocks without edges; and  
5 computation logic to compute a set of output colors that best renders  
6 a color average of the input image for the corresponding block.

**ABSTRACT OF THE DISCLOSURE**

A method and apparatus for block quantization for color halftoning is described. In one embodiment, the method comprises dividing an input image and an output image into blocks, wherein each block in the output image corresponds to one block in the input image. The method also includes dividing each block into subblocks if an edge exists in said each block, calculating a color average of each input block, and calculating a set of output colors for each block in the output image to match the color average of its corresponding block in the input image.



AVIAD

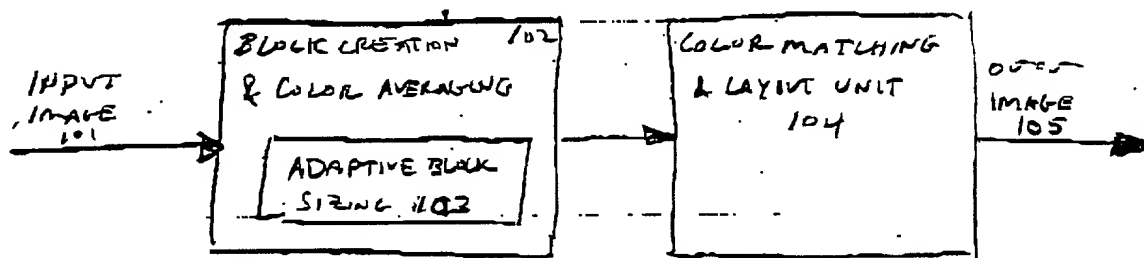
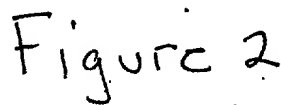


FIGURE 1







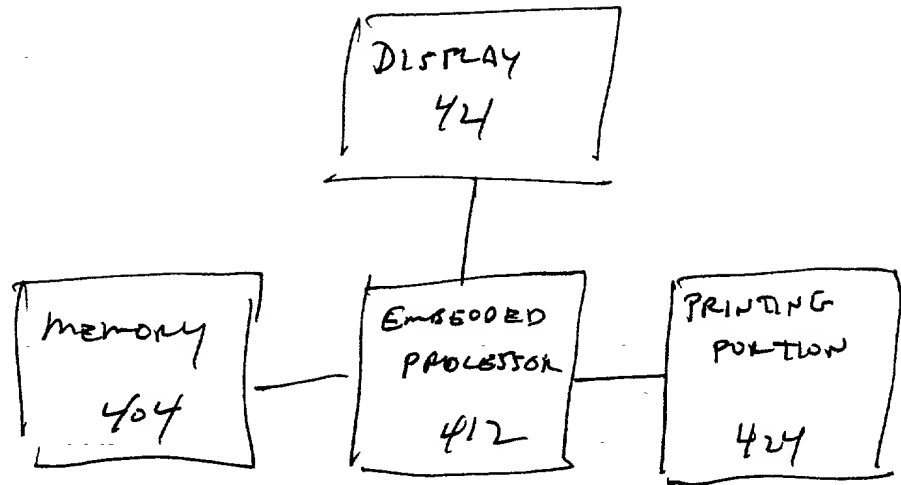


Figure 4

Example  
Dither matrix

1	8	7	4
3	14	10	15
6	12	5	9
13	2	0	11

(a)

Chosen  
Output color set  
0W  
7Y  
2C  
3M  
→ 4B →

Y	C	C	Y
Y	B	M	B
Y	B	Y	M
B	Y	Y	M

(b)

FIGURE 5

$\frac{1}{24}$	$\frac{1}{24}$	$\frac{1}{24}$	$\frac{1}{24}$
$\frac{1}{24}$	$\frac{1}{8}$	$\frac{1}{8}$	$\frac{1}{24}$
$\frac{1}{24}$	$\frac{1}{8}$	$\frac{1}{8}$	$\frac{1}{24}$
$\frac{1}{24}$	$\frac{1}{24}$	$\frac{1}{24}$	$\frac{1}{24}$

Figure 6

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below, next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled  
A BLOCK QUANTIZATION METHOD FOR COLOR HALFTONING

the specification of which

X is attached hereto.  
\_\_\_\_\_ was filed on \_\_\_\_\_ as  
United States Application Number \_\_\_\_\_  
or PCT International Application Number \_\_\_\_\_  
and was amended on \_\_\_\_\_  
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above. I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof, or patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, and that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months (for a utility patent application) or six months (for a design patent application) prior to this application.

I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d), of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

<u>Prior Foreign Application(s)</u>			<u>Priority Claimed</u>	
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	Yes	No
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	Yes	No
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	Yes	No

I hereby claim the benefit under title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below:

_____ (Application Number)	_____ Filing Date
_____ (Application Number)	_____ Filing Date

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

_____ (Application Number)	_____ Filing Date	_____ (Status -- patented, pending, abandoned)
_____ (Application Number)	_____ Filing Date	_____ (Status -- patented, pending, abandoned)

I hereby appoint the persons listed on Appendix A hereto (which is incorporated by reference and a part of this document) as my respective patent attorneys and patent agents, with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

Send correspondence to Michael J. Mallie, BLAKELY, SOKOLOFF, TAYLOR &  
 (Name of Attorney or Agent)  
 ZAFMAN LLP, 12400 Wilshire Boulevard 7th Floor, Los Angeles, California 90025 and direct  
 telephone calls to Michael J. Mallie, (408) 720-8300.  
 (Name of Attorney or Agent)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of Sole/First Inventor Maya Rani Gupta

Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

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Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

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Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

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(City, State) (Country)

Post Office Address \_\_\_\_\_  
\_\_\_\_\_

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Inventor's Signature \_\_\_\_\_ Date \_\_\_\_\_

Residence \_\_\_\_\_ Citizenship \_\_\_\_\_  
(City, State) (Country)

Post Office Address \_\_\_\_\_  
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## APPENDIX A

William E. Alford, Reg. No. 37,764; Farzad E. Amini, Reg. No. P42,261; Aloysius T. C. AuYeung, Reg. No. 35,432; William Thomas Babbitt, Reg. No. 39,591; Carol F. Barry, Reg. No. 41,600; Jordan Michael Becker, Reg. No. 39,602; Bradley J. Berezna, Reg. No. 33,474; Michael A. Bernadicou, Reg. No. 35,934; Roger W. Blakely, Jr., Reg. No. 25,831; Gregory D. Caldwell, Reg. No. 39,926; Ronald C. Card, Reg. No. 44,587; Andrew C. Chen, Reg. No. 43,544; Thomas M. Coester, Reg. No. 39,637; Alin Corie, Reg. No. P46,244; Dennis M. deGuzman, Reg. No. 41,702; Stephen M. De Klerk, under 37 C.F.R. § 10.9(b); Michael Anthony DeSanctis, Reg. No. 39,957; Daniel M. De Vos, Reg. No. 37,813; Robert Andrew Diehl, Reg. No. 40,992; Sanjeet Dutta, Reg. No. P46,145; Matthew C. Fagan, Reg. No. 37,542; Tarek N. Fahmi, Reg. No. 41,402; Paramita Ghosh, Reg. No. 42,806; James Y. Go, Reg. No. 40,621; James A. Henry, Reg. No. 41,064; Willmore F. Holbrow III, Reg. No. P41,845; Sheryl Sue Holloway, Reg. No. 37,850; George W Hoover II, Reg. No. 32,992; Eric S. Hyman, Reg. No. 30,139; William W. Kidd, Reg. No. 31,772; Sang Hui Kim, Reg. No. 40,450; Eric T. King, Reg. No. 44,188; Erica W. Kuo, Reg. No. 42,775; Kurt P. Leyendecker, Reg. No. 42,799; Michael J. Mallie, Reg. No. 36,591; Andre L. Marais, under 37 C.F.R. § 10.9(b); Paul A. Mendonsa, Reg. No. 42,879; Darren J. Milliken, Reg. No. 42,004; Lisa A. Norris, Reg. No. 44,976; Chun M. Ng, Reg. No. 36,878; Thien T. Nguyen, Reg. No. 43,835; Thinh V. Nguyen, Reg. No. 42,034; Dennis A. Nicholls, Reg. No. 42,036; Daniel E. Ovanezian, Reg. No. 41,236; Marina Portnova, Reg. No. P45,750; Babak Redjaian, Reg. No. 42,096; William F. Ryann, Reg. No. 44,313; James H. Salter, Reg. No. 35,668; William W. Schaal, Reg. No. 39,018; James C. Scheller, Reg. No. 31,195; Jeffrey Sam Smith, Reg. No. 39,377; Maria McCormack Sobrino, Reg. No. 31,639; Stanley W. Sokoloff, Reg. No. 25,128; Judith A. Szepesi, Reg. No. 39,393; Vincent P. Tassinari, Reg. No. 42,179; Edwin H. Taylor, Reg. No. 25,129; John F. Travis, Reg. No. 43,203; George G. C. Tseng, Reg. No. 41,355; Joseph A. Twarowski, Reg. No. 42,191; Lester J. Vincent, Reg. No. 31,460; Glenn E. Von Tersch, Reg. No. 41,364; John Patrick Ward, Reg. No. 40,216; Mark L. Watson, Reg. No. P46,322; Thomas C. Webster, Reg. No. P46,154; Charles T. J. Weigell, Reg. No. 43,398; Kirk D. Williams, Reg. No. 42,229; James M. Wu, Reg. No. 45,241; Steven D. Yates, Reg. No. 42,242; and Norman Zafman, Reg. No. 26,250; my patent attorneys, and Justin M. Dillon, Reg. No. 42,486; my patent agent, of BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP, with offices located at 12400 Wilshire Boulevard, 7th Floor, Los Angeles, California 90025, telephone (310) 207-3800, and James R. Thein, Reg. No. 31,710, my patent attorney.

## APPENDIX B

### Title 37, Code of Federal Regulations, Section 1.56 Duty to Disclose Information Material to Patentability

(a) A patent by its very nature is affected with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclosure information exists with respect to each pending claim until the claim is cancelled or withdrawn from consideration, or the application becomes abandoned. Information material to the patentability of a claim that is cancelled or withdrawn from consideration need not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability of any existing claim. The duty to disclose all information known to be material to patentability is deemed to be satisfied if all information known to be material to patentability of any claim issued in a patent was cited by the Office or submitted to the Office in the manner prescribed by §§1.97(b)-(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:

- (1) Prior art cited in search reports of a foreign patent office in a counterpart application, and
  - (2) The closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein is disclosed to the Office.
- (b) Under this section, information is material to patentability when it is not cumulative to information already of record or being made of record in the application, and
- (1) It establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim; or
  - (2) It refutes, or is inconsistent with, a position the applicant takes in:
    - (i) Opposing an argument of unpatentability relied on by the Office, or
    - (ii) Asserting an argument of patentability.

A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability.

(c) Individuals associated with the filing or prosecution of a patent application within the meaning of this section are:

- (1) Each inventor named in the application;
- (2) Each attorney or agent who prepares or prosecutes the application; and
- (3) Every other person who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.

(d) Individuals other than the attorney, agent or inventor may comply with this section by disclosing information to the attorney, agent, or inventor.